

Electronic Acknowledgement Receipt

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Application Number:	10801503
International Application Number:	
Confirmation Number:	1822
Title of Invention:	Fractional-type Phase-Locked Loop circuit with compensation of phase errors
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File Listing:					
Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		2110-111-03_20090401_fOAResp_20090701_as_filed.pdf	193556 87ed0a6d4848add027028cab725ae6191184019	yes	20
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Amendment/Req. Reconsideration-After Non-Final Reject		1	1	
	Claims		2	9	
	Applicant Arguments/Remarks Made in an Amendment		10	20	
Warnings:					
Information:					
2	Fee Worksheet (PTO-875)	fee-info.pdf	29927 ed76a95c2bc3b9801ea332316cef943515ce6318	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			223483		

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New International Application Filed with the USPTO as a Receiving Office

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